

**U.S. UTILITY Patent Application**

**O.I.P.E.**

**PATENT DATE**

SCANNED

W. A. Me

APPLICATION NO.  
09/538670

**CONT/PRIOR**

CLASS  
712

SUBCLASS

ART UNIT

**EXAMINER**

## APPLICANTS

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**TITLE**

Method and apparatus for allocating functional units in a multithreaded vliw processor

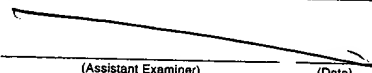

PTO-2040  
12/89

**ISSUING CLASSIFICATION**

ISSUING CLASSIFICATION

ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
712		24		718	103	107						
INTERNATIONAL CLASSIFICATION												
G06F	IT/00											
G06F	9/46											

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg. <b>5</b>	Figs. Drwg. <b>9</b>	Print Fig. <b>7B</b>	Total Claims <b>16</b>	Print Claim for O.G. <b>1</b>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	 _____ (Assistant Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	<b>LARRY D. DONAGHUE</b> <b>PRIMARY EXAMINER</b>  _____ (Primary Examiner) _____ (Date)			<b>6/8/05</b>	
				<b>ISSUE FEE</b>	
				Amount Due <b>1400.00</b>	Date Paid _____
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			<b>ISSUE BATCH NUMBER</b>	
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